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Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

PTOL-90A (Rev. 04/07)

	Application No.	Applicant(s)
Office Action Summary	10/686,402	HOLEMAN, JAMES L.
	Examiner	Art Unit	
	Kan Yuen	2616	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE <u>3</u> MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.
- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133).
  Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

1)☒ Responsive to communication(s) filed on *15 October 2003*.

2a)☐ This action is **FINAL**.   2b)☒ This action is non-final.

3)☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

4)☒ Claim(s) *1-28* is/are pending in the application.

    4a) Of the above claim(s) _____ is/are withdrawn from consideration.

5)☐ Claim(s) _____ is/are allowed.

6)☒ Claim(s) *1-28* is/are rejected.

7)☐ Claim(s) _____ is/are objected to.

8)☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

9)☐ The specification is objected to by the Examiner.

10)☒ The drawing(s) filed on *15 October 2003* is/are: a)☒ accepted or b)☐ objected to by the Examiner.

    Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).

    Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).

11)☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

12)☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).

    a)☐ All   b)☐ Some * c)☐ None of:

        1.☐ Certified copies of the priority documents have been received.

        2.☐ Certified copies of the priority documents have been received in Application No. _____.

        3.☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

    * See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

1) ☒ Notice of References Cited (PTO-892)

2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)

3) ☒ Information Disclosure Statement(s) (PTO/SB/08)
    Paper No(s)/Mail Date *02/23/2004*.

4) ☐ Interview Summary (PTO-413)
    Paper No(s)/Mail Date. _____.

5) ☐ Notice of Informal Patent Application

6) ☐ Other: _____.

Detailed Action

Claim Rejections - 35 USC § 103

1.   The factual inquiries set forth in *Graham* v. *John Deere Co.*, 383 U.S. 1, 148

USPQ 459 (1966), that are applied for establishing a background for determining

obviousness under 35 U.S.C. 103(a) are summarized as follows:

> 1.   Determining the scope and contents of the prior art.
> 2.   Ascertaining the differences between the prior art and the claims at issue.
> 3.   Resolving the level of ordinary skill in the pertinent art.
> 4.   Considering objective evidence present in the application indicating obviousness or nonobviousness.

2.   The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all

obviousness rejections set forth in this Office action:

> (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

3.   Claims 1, 4, 6, 8-13, 15, 18-22, 24, 27, and 28 are rejected under 35 U.S.C.

103(a) as being unpatentable over Aiello et al (Pub No.: 2005/0276255), in view of

Bilgic (Pat No.: 6256492).

For claim 1 Aiello et al. disclosed the method of a first plurality of bits and a

second plurality of bits, the first and second plurality of bits addressing a range of nodes

that should respond to the node register command (Aiello et al. see paragraph 0071,

lines 1-15, and see fig. 4), As revealed in the reference, the TDMA frames has broken

down to many sections, the sections 64a-n provides data transmission time for a

corresponding slave device 14a-n of network 10; and responsive to detecting a

response, determining a responding node corresponding to the response and

designating the responding node as registered (Aiello et al. see paragraph 0078, lines

1-25), as revealed in the paragraph, the unregistered slave device transmits a

response, which can be interpreted as unregistered device determines that they should

response. However, Aiello et al. did not disclosed the method of a plurality of time slot

delays; during each of the plurality of time slot delays, listening to the network for a

response from a node in the range of nodes, the node determined by each of the

plurality of time slot delays. Bilgic from the same or similar fields of endeavor teaches

the method of a plurality of time slot delays; during each of the plurality of time slot

delays, listening to the network for a response from a node in the range of nodes, the

node determined by each of the plurality of time slot delays (Bilgic see fig. 2, and see

column 4, lines 66-67, and see column 5, lines 1-10), as shown in the reference, the

frame is broken down into slot for duplex communication between base station and

mobile station, therefore we can interpreted that each slot has two section, one is

communication forward, and the other is backward. Thus, it would have been obvious to

the person of ordinary skill in the art at the time of the invention to use the method as

taught by Bilgic in the network of Aiello et al. The motivation for using the method as

taught by Bilgic in the network of Aiello et al. being that it provides simultaneously

responses from the slave devices to the master device.

Regarding to claim 4, Aiello et al. disclosed the method of transmitting the

confirmation packet; during each of the second plurality of time slot delays, transmitting

a signal based on a registration status of a corresponding node, the signal being a

confirmation of the registration of the corresponding node (Aiello et al. see paragraph

0078, lines 1-25), as revealed in the paragraph, the master transmits a confirmation

command to the slave device to indicate that the slave is in status "online". However,

Aiello et al. did not disclose the method of a second plurality of time slot delays. Bilgic

from the same or similar fields of endeavor teaches the method of a second plurality of

time slot delays (Bilgic see fig. 2, and see column 4, lines 66-67, and see column 5,

lines 1-10), as shown in the reference, the frame is broken down into slot for duplex

communication between base station and mobile station, therefore we can interpreted

that each slot has two section, one is communication forward, and the other is

backward. Thus, it would have been obvious to the person of ordinary skill in the art at

the time of the invention to use the method as taught by Bilgic in the network of Aiello et

al. The motivation for using the method as taught by Bilgic in the network of Aiello et al.

being that it provides simultaneously responses from the slave devices to the master

device.

Regarding to claim 6, Aiello et al. all the subject matter of the claimed invention

with the exception of each of the plurality of time slot delays is a response period during

which at most one node may transmit a message in response to the node register

command. Bilgic from the same or similar fields of endeavor teaches the method of

each of the plurality of time slot delays is a response period during which at most one

node may transmit a message in response to the node register command (Bilgic see fig.

2, and see column 4, lines 66-67, and see column 5, lines 1-10), as shown in the

reference, the frame is broken down into slot for duplex communication between base

station and mobile station, therefore we can interpreted that each slot has two section,

one is communication forward, and the other is backward. Thus, it would have been

obvious to the person of ordinary skill in the art at the time of the invention to use the

method as taught by Bilgic in the network of Aiello et al. The motivation for using the

method as taught by Bilgic in the network of Aiello et al. being that it provides

simultaneously responses from the slave devices to the master device.

Regarding to claim 8, Aiello et al. disclosed the method of at a node, receiving a

node register command addressing a range of nodes, the node register command

comprising a plurality of time slot delays; determining whether the node is in the range

of addressed nodes (Aiello et al. see paragraph 0071, lines 1-15, and see fig. 4), As

revealed in the reference, the TDMA frames has broken down to many sections, the

sections 64a-n provides data transmission time for a corresponding slave device 14a-n

of network 10 ; and responsive to determining that the node is in the range of addressed

nodes, waiting for a proper time slot delay, the proper time slot delay being one of the

plurality of time slot delays, the proper time slot delay corresponds to the node (Aiello et

al. see paragraph 0078, lines 1-25, and see fig. 4); As revealed in the reference, the

master device 12 periodically broadcasts an ALOHA message to invite slave devices to

send their pending protocol messages for registration. The ALOHA broadcast is

transmitted in command section 26 in every three seconds. Also, in fig. 3a the free time

blocks 36a through 36n separates the assigned data slots 30a through 30n. Therefore

we can interpret the time blocks 36a-n is the time slot delays. However Aiello et al. did

not disclose the method of transmitting a message during the proper time slot delay, the

message being a response to the node register command. Bilgic from the same or similar fields of endeavor teaches the method of transmitting a message during the proper time slot delay, the message being a response to the node register command (Bilgic see fig. 2, and see column 4, lines 66-67, and see column 5, lines 1-10), as shown in the reference, the frame is broken down into slot for duplex communication between base station and mobile station, therefore we can interpreted that each slot has two section, one is communication forward, and the other is backward. Thus, it would have been obvious to the person of ordinary skill in the art at the time of the invention to use the method as taught by Bilgic in the network of Aiello et al. The motivation for using the method as taught by Bilgic in the network of Aiello et al. being that it provides simultaneously responses from the slave devices to the master device.

Regarding to claim 9, Aiello et al. disclosed the method of the message is a `true` signal (Aiello et al. see paragraph 0078, lines 1-25, and see fig. 4), the true signal can be interpreted as the response from the slave device is successfully transmitted to the master device.

Regarding to claim 10, Aiello et al. disclosed the method of setting a tentatively registered flag (Aiello et al. see paragraph 0078, lines 1-25, and see fig. 4). As shown in the paragraph, the status of the slave device is maintained and tracked by master state table, which it can be interpreted as registered flag.

Regarding to claim 11, Aiello et al. disclosed the method of receiving a confirmation packet comprising a second plurality of time slot delays (Aiello et al. see paragraph 0078, lines 1-25, and see fig. 4); during the assigned time slot delay,

listening for a confirmation message (Aiello et al. see paragraph 0078, lines 1-25, and

see fig. 4), after the slave device transmitted a response, it is waiting for the

confirmation message from the master, which can be interpreted as listening for

confirmation message; and responsive to detecting a confirmation message, setting a

registered flag (Aiello et al. see paragraph 0078, lines 1-25, and see fig. 4), after the

slave is registered, the master stores the status in the master table. However, Aiello et

al. did not disclose the method of a second plurality of time slot delays; waiting for an

assigned time slot delay, the assigned time slot delay being one of the second plurality

of time slot delays. Bilgic from the same or similar fields of endeavor teaches the

method of waiting for an assigned time slot delay, the assigned time slot delay being

one of the second plurality of time slot delays (Bilgic see fig. 2, and see column 4, lines

66-67, and see column 5, lines 1-10), as shown in the reference, the frame is broken

down into slot for duplex communication between base station and mobile station,

therefore we can interpreted that each slot has two section, one is communication

forward, and the other is backward. Therefore, this method can be used in confirmation

packet as taught by Aiello et al. Thus, it would have been obvious to the person of

ordinary skill in the art at the time of the invention to use the method as taught by Bilgic

in the network of Aiello et al. The motivation for using the method as taught by Bilgic in

the network of Aiello et al. being that it provides simultaneously responses from the

slave devices to the master device.

Regarding to claim 12, Aiello et al. disclosed the method of the confirmation

message is a 'true' signal (Aiello et al. see paragraph 0078, lines 1-25, and see fig. 4),

the true signal can be interpreted as the response from the slave device is successfully

transmitted to the master device.

Regarding to claim 13, Aiello et al. disclosed the method of using executable

program or software (see paragraph 0070, lines 1-10) of a like to transmit a node

register command over a network, the node register command comprising: a first

plurality of bits and a second plurality of bits, the first and second plurality of bits

addressing a range of nodes that should respond to the node register command (Aiello

et al. see paragraph 0071, lines 1-15, and see fig. 4), As revealed in the reference, the

TDMA frames has broken down to many sections, the sections 64a-n provides data

transmission time for a corresponding slave device 14a-n of network 10; and during

each of the plurality of time slot delays, listen to the network for the response message

(Aiello et al. see paragraph 0078, lines 1-25, and see fig. 4), after the slave device

transmitted a response, it is waiting for the confirmation message from the master,

which can be interpreted as listening for confirmation message; and responsive to

detecting a response message, determine a responding one of the range of nodes that

transmitted the response message and designate the one of the range of nodes as

registered (Aiello et al. see paragraph 0078, lines 1-25), as revealed in the paragraph,

in order for the slave devices to communicate with each other, they must be registered

with the master device. The master device comprises a master state table, which

indicates a record of if the slave devices are currently registered or not. However, Aiello

et al. did not disclose the method of a plurality of time slot delays, each of the plurality of

time slot delays designated for one of the range of nodes to transmit a response

message. Bilgic from the same or similar fields of endeavor teaches the method of a

plurality of time slot delays, each of the plurality of time slot delays designated for one of

the range of nodes to transmit a response message (Bilgic see fig. 2, and see column 4,

lines 66-67, and see column 5, lines 1-10), as shown in the reference, the frame is

broken down into slot for duplex communication between base station and mobile

station, therefore we can interpreted that each slot has two section, one is

communication forward, and the other is backward. Therefore, this method can be used

in confirmation packet as taught by Aiello et al. Thus, it would have been obvious to the

person of ordinary skill in the art at the time of the invention to use the method as taught

by Bilgic in the network of Aiello et al. The motivation for using the method as taught by

Bilgic in the network of Aiello et al. being that it provides simultaneously responses from

the slave devices to the master device.

Regarding to claim 15, Aiello et al. disclosed the method of create a confirmation

packet comprising a second plurality of time slot delays (Aiello et al. see paragraph

0078, lines 1-25, and see fig. 4); transmit the confirmation packet; and during each of

the second plurality of time slot delays, transmit a confirmation message to indicate

confirmation of the registration of a corresponding node, the corresponding node being

one of the range of nodes (Aiello et al. see paragraph 0078, lines 1-25), as revealed in

the paragraph, the master transmits a confirmation command to the slave device to

indicate that the slave is in status "online".

Regarding to claim 18, Aiello et al. disclosed the method of a computer-readable

storage medium having stored thereon computer instructions that, when executed by a

computer (Aiello et al. see paragraph 0070, lines 1-15), cause the computer to: receive

a node register command addressing a range of nodes, the node register command

comprising a plurality of time slot delays (Aiello et al. see paragraph 0071, lines 1-15,

and see fig. 4), As revealed in the reference, the TDMA frames has broken down to

many sections, the sections 64a-n provides data transmission time for a corresponding

slave device 14a-n of network 10; determine whether to respond to the node register

command (Aiello et al. see paragraph 0078, lines 1-25), as revealed in the paragraph,

the unregistered slave device transmits a response, which can be interpreted as

unregistered device determines that they should response; and responsive to

determining to respond to the node register command, transmit a message during an

assigned time slot delay, the assigned time slot delay being one of the plurality of time

slot delays, the message being a response to the node register command (Aiello et al.

see paragraph 0078, lines 1-25), as revealed in the paragraph, in order for the slave

devices to communicate with each other, they must be registered with the master

device. The master device comprises a master state table, which indicates a record of if

the slave devices are currently registered or not. However, Aiello et al. did not disclose

the method of a plurality of time slot delays. Bilgic from the same or similar fields of

endeavor teaches the method of a plurality of time slot delays (Bilgic see fig. 2, and see

column 4, lines 66-67, and see column 5, lines 1-10), as shown in the reference, the

frame is broken down into slot for duplex communication between base station and

mobile station, therefore we can interpreted that each slot has two section, one is

communication forward, and the other is backward. Therefore, this method can be used

in confirmation packet as taught by Aiello et al. Thus, it would have been obvious to the

person of ordinary skill in the art at the time of the invention to use the method as taught

by Bilgic in the network of Aiello et al. The motivation for using the method as taught by

Bilgic in the network of Aiello et al. being that it provides simultaneously responses from

the slave devices to the master device.

Regarding to claim 19, Aiello et al. disclosed the method of the computer

instructions that receive the node register command further comprise computer

instructions that, when executed by a computer, cause the computer to set a tentatively

registered flag (Aiello et al. see paragraph 0078, lines 1-25, and see fig. 4). As shown in

the paragraph, the status of the slave device is maintained and tracked by master state

table, which it can be interpreted as registered flag.

Regarding to claim 20, Aiello et al. disclosed the method of receive a

confirmation packet comprising a second plurality of time slot delays; wait for an

assigned time slot delay, the assigned time slot delay being one of the second plurality

of time slot delays (Aiello et al. see paragraph 0078, lines 1-25, and see fig. 4); during

the assigned time slot delay, listening for a confirmation message (Aiello et al. see

paragraph 0078, lines 1-25, and see fig. 4), after the slave device transmitted a

response, it is waiting for the confirmation message from the master, which can be

interpreted as listening for confirmation message; and responsive to detecting a

confirmation message, setting a registered flag (Aiello et al. see paragraph 0078, lines

1-25, and see fig. 4), after the slave is registered, the master stores the status in the

master table. However, Aiello et al. did not disclose the method of a plurality of time slot

delays. Bilgic from the same or similar fields of endeavor teaches the method of a

plurality of time slot delays (Bilgic see fig. 2, and see column 4, lines 66-67, and see

column 5, lines 1-10), as shown in the reference, the frame is broken down into slot for

duplex communication between base station and mobile station, therefore we can

interpreted that each slot has two section, one is communication forward, and the other

is backward. Therefore, this method can be used in confirmation packet as taught by

Aiello et al. Thus, it would have been obvious to the person of ordinary skill in the art at

the time of the invention to use the method as taught by Bilgic in the network of Aiello et

al. The motivation for using the method as taught by Bilgic in the network of Aiello et al.

being that it provides simultaneously responses from the slave devices to the master

device.

Regarding to claim 21, Aiello et al. disclosed the method of the confirmation

message is a `1` signal (Aiello et al. see paragraph 0078, lines 1-25, and see fig. 4), the

1 signal can be interpreted as the response from the slave device is successfully

transmitted to the master device.

Regarding to claim 22, Aiello et al. disclosed the method of

the message is a `1` signal (Aiello et al. see paragraph 0078, lines 1-25, and see

fig. 4), the 1 signal can be interpreted as the response from the slave device is

successfully transmitted to the master device.

Regarding to claim 24, Aiello et al. disclosed the method of a system for

deterministic registration for communication networks comprising: a means for

transmitting a node register command over a network, the node register command

addressing a plurality of nodes that should respond to the node register command, the

node register command comprising a plurality of time slot delays, each of the plurality of

time slot delays designated for one of the plurality of addressed nodes to transmit a

response message (Aiello et al. see paragraph 0071, lines 1-15, and see fig. 4), As

revealed in the reference, the TDMA frames has broken down to many sections, the

sections 64a-n provides data transmission time for a corresponding slave device 14a-n

of network 10; and a means for detecting a transmission of a response message during

each of the plurality of time slot delays; a means for determining a responding node

responsive to detecting a response message, the responding node being one of the

plurality of addressed nodes (Aiello et al. see paragraph 0078, lines 1-25), as revealed

in the paragraph, the unregistered slave device transmits a response, which can be

interpreted as unregistered device determines that they should response; and a means

for designating the responding node as registered (Aiello et al. see paragraph 0078,

lines 1-25), as revealed in the paragraph, in order for the slave devices to communicate

with each other, they must be registered with the master device. The master device

comprises a master state table, which indicates a record of if the slave devices are

currently registered or not. However, Aiello et al. did not disclose the method of a

plurality of time slot delays. Bilgic from the same or similar fields of endeavor teaches

the method of a plurality of time slot delays (Bilgic see fig. 2, and see column 4, lines

66-67, and see column 5, lines 1-10), as shown in the reference, the frame is broken

down into slot for duplex communication between base station and mobile station,

therefore we can interpreted that each slot has two section, one is communication

forward, and the other is backward. Therefore, this method can be used in confirmation

packet as taught by Aiello et al. Thus, it would have been obvious to the person of

ordinary skill in the art at the time of the invention to use the method as taught by Bilgic

in the network of Aiello et al. The motivation for using the method as taught by Bilgic in

the network of Aiello et al. being that it provides simultaneously responses from the

slave devices to the master device.

Regarding to claim 25, Aiello et al. disclosed the method of a means for creating

a confirmation packet comprising a second plurality of time slot delays (Aiello et al. see

paragraph 0078, lines 1-25, and see fig. 4); a means for transmitting the confirmation

packet; and a means for transmitting a confirmation message to indicate confirmation of

the registration of the responding node during an appropriate one of the second plurality

of time slot delays (Aiello et al. see paragraph 0078, lines 1-25), as revealed in the

paragraph, in order for the slave devices to communicate with each other, they must be

registered with the master device. The master device comprises a master state table,

which indicates a record of if the slave devices are currently registered or not.

Regarding to claim 27, Aiello et al. disclosed the method of system for registering

during a deterministic registration process comprising: a means for receiving a node

register command, the node register command addresses a plurality of nodes, the node

register command comprising a plurality of time slot delays (Aiello et al. see paragraph

0078, lines 1-25, and see fig. 4); a means for determining whether to respond to the

node register command; and a means for transmit a message during an assigned time

slot delay in response to determining to respond to the node register command, the

assigned time slot delay being one of the plurality of time slot delays, the message

being a response to the node register command (Aiello et al. see paragraph 0078, lines

1-25), as revealed in the paragraph, the unregistered slave device transmits a

response, which can be interpreted as unregistered device determines that they should

response. However, Aiello et al. did not disclose the method of a plurality of time slot

delays. Bilgic from the same or similar fields of endeavor teaches the method of a

plurality of time slot delays (Bilgic see fig. 2, and see column 4, lines 66-67, and see

column 5, lines 1-10), as shown in the reference, the frame is broken down into slot for

duplex communication between base station and mobile station, therefore we can

interpreted that each slot has two section, one is communication forward, and the other

is backward. Therefore, this method can be used in confirmation packet as taught by

Aiello et al. Thus, it would have been obvious to the person of ordinary skill in the art at

the time of the invention to use the method as taught by Bilgic in the network of Aiello et

al. The motivation for using the method as taught by Bilgic in the network of Aiello et al.

being that it provides simultaneously responses from the slave devices to the master

device.

Regarding to claim 28, Aiello et al. disclosed the method of a means for receiving

a confirmation packet, the confirmation packet comprising a second plurality of time slot

delays (Aiello et al. see paragraph 0078, lines 1-25, and see fig. 4); a means for

detecting a confirmation message during an assigned time slot delay, the assigned time

slot delay being one of the plurality of time slot delays; and a means for setting a

registered flag in response to detecting the confirmation message (Aiello et al. see

paragraph 0078, lines 1-25), as revealed in the paragraph, the master transmits a

confirmation command to the slave device to indicate that the slave is in status "online".

However, Aiello et al. did not disclose the method of a plurality of time slot delays. Bilgic

from the same or similar fields of endeavor teaches the method of a plurality of time slot

delays (Bilgic see fig. 2, and see column 4, lines 66-67, and see column 5, lines 1-10),

as shown in the reference, the frame is broken down into slot for duplex communication

between base station and mobile station, therefore we can interpreted that each slot

has two section, one is communication forward, and the other is backward. Therefore,

this method can be used in confirmation packet as taught by Aiello et al. Thus, it would

have been obvious to the person of ordinary skill in the art at the time of the invention to

use the method as taught by Bilgic in the network of Aiello et al. The motivation for

using the method as taught by Bilgic in the network of Aiello et al. being that it provides

4.   Claims 2, 3, 7, 14, and 17 are rejected under 35 U.S.C. 103(a) as being

unpatentable over Aiello et al (Pub No.: 2005/0276255), in view of Bilgic (Pat No.:

6256492), as applied to claim 1 above, and further in view of Padovani et al. (Pat No.:

6574211).

For claim 2, Aiello et al. and Bilgic disclosed all the subject matter of the claimed

invention with the exception of the second plurality of bits are padded with zeros.

Padovani et al. from the same or similar fields of endeavor teaches the method of the

second plurality of bits are padded with zeros (Padovani et al. see column 24, lines 26-

32). Thus, it would have been obvious to the person of ordinary skill in the art at the

time of the invention to use the method as taught by Padovani et al. in the network of

Aiello et al. and Bilgic. The motivation for using the method as taught by Padovani et al.

in the network of Aiello et al. and Bilgic being that it provide uniform data slots in the

system.

Regarding to claim 3, Padovani et al. also disclosed the method of the node

register command further comprises a third plurality of bits (Padovani et al. see column

21 lines 23-35, and see fig. 4A). In the figure 4A, the traffic channel is fragmented into

plurality of frames, and each frame comprises 16 time slots and each time slot is further

fragmented into 4 quarter slots, therefore we can interpreted that each quarter slot

represents each bit, and there are 16 sets of quarter bits in this frame. Thus, it would

have been obvious to the person of ordinary skill in the art at the time of the invention to

use the method as taught by Padovani et al. in the network of Aiello et al. and Bilgic.

The motivation for using the method as taught by Padovani et al. in the network of Aiello

et al. and Bilgic being that it performs simultaneous tasks at each time slot.

Regarding to claim 7, Padovani et al. also disclosed the method of during each of

the second plurality of time slot delays, not transmitting a signal if the corresponding

node is not registered (Padovani et al. see column 7, lines 1-6).

Regarding to claim 14, Padovani et al. also disclosed the method of the second

plurality of bits is padded with zeros (Padovani et al. see column 24, lines 26-32).

Regarding to claim 16, Padovani et al. also disclosed the method of during a

portion of each of the plurality of time slot delays, determine a level of noise in the

network; and set a threshold for a good signal, the threshold being above the level of noise (Padovani et al. see column 4, lines 26-43), since the reference also teaches the duplex communication between the base and mobile stations, we can call at each time slot, the slot comprise of two portion, and one of the portion can be the time slot delay. Moreover, the mobile measures the C/I of the forward link signals, which can be interpreted as, determine a level of noise. The mobile also selects the best base station using highest data rate, which is the threshold.

Regarding to claim 17, Padovani et al. also disclosed the method of each of the plurality of time slot delays comprises a calibration period and a response transmission period (Padovani et al. see column 16, lines 48-67, and see column 17, lines 1-7).

5.   Claim 23 rejected under 35 U.S.C. 103(a) as being unpatentable over Podovani et al. (Pat No.: 6574211), in view of Bilgic (Pat No.: 6256492).

For claim 23, Podovani et al. disclosed the method of determining a ceiling of the level of ambient noise; setting a threshold for a good signal to a predetermined level above the ceiling of the level of ambient noise (Padovani et al. see column 4, lines 66-67, and see column 6, lines 49-67), since the reference also teaches the duplex communication between the base and mobile stations, we can call at each time slot, the slot comprise of two portion, and one of the portion can be the time slot delay. Moreover, the mobile measures the C/I of the forward link signals, which can be interpreted as, determine a level of noise. The mobile also selects the best base station

using highest data rate, which means the mobile sets a highest threshold level. The

highest threshold level can be ceiling of level; and during a second portion of the time

slot delay, listening to the network for a signal (Padovani et al. see column 5, lines 44-

67), as shown in the reference, the mobile is waiting for a signal from base station at

different time slots. The time slots can be interpreted as time slot delays. However,

Padovani et al. did not disclose the method of a plurality of time slot delays shows two

portion of time slot. Bilgic from the same or similar fields of endeavor teaches the

method of a plurality of time slot delays (Bilgic see fig. 2, and see column 4, lines 66-67,

and see column 5, lines 1-10), as shown in the reference, the frame is broken down into

slot for duplex communication between base station and mobile station, and each slot

has two portions, and therefore we can interpreted that each slot has two section, one is

communication forward, and the other is backward. Thus, it would have been obvious to

the person of ordinary skill in the art at the time of the invention to use the method as

taught by Bilgic in the network of Padovani et al. The motivation for using the method as

taught by Bilgic in the network of Padovani et al. being that it provides simultaneously

responses from the slave devices to the master device.

6.   Claims 5 and 26 are rejected under 35 U.S.C. 103(a) as being unpatentable over

Aiello et al (Pub No.: 2005/0276255), in view of Bilgic (Pat No.: 6256492), as applied to

claim 1 above, and further in view of Gehring et al. (Pub No.: 2004/0090983).

For claim 5, Aiello et al. and Bilgic disclosed all the subject matter of the claimed invention with the exception of during each of the plurality of time slot delays, calibrating a receiver during a first portion of each of the plurality of time slot delays. Gehring et al. from the same of ordinary skill in the art at the time of the invention teaches the method of during each of the plurality of time slot delays, calibrating a receiver during a first portion of each of the plurality of time slot delays (Gahring et al. see paragraph 0069, lines 1-15). The reference teaches that the slave sync symbols 42a-n are used by a source slave device for providing timing synchronization signals to a corresponding target slave device to accommodate for propagation delays. Therefore, we can interpret that during the delay, the sync symbols are used between target and source slave devices to accommodate (calibrate) for propagation delays. Thus, it would have been obvious to the person of ordinary skill in the art at the time of the invention to use the method as taught by Gehring et al. in the network of Aiello et al. and Bilgic. The motivation for using the method as taught by Gehring et al. in the network of Aiello et al. and Bilgic being that balance the communication between devices compatibility in the system.

Regarding to claim 26, Gehring et al. also disclosed the method of a means for calibrating a receiver during a portion of each of the plurality of time slot delays (Gahring et al. see paragraph 0069, lines 1-15). The reference teaches that the slave sync symbols 42a-n are used by a source slave device for providing timing synchronization signals to a corresponding target slave device to accommodate for propagation delays. Therefore, we can interpret that during the delay, the sync symbols are used between

target and source slave devices to accommodate (calibrate) for propagation delays.

Thus, it would have been obvious to the person of ordinary skill in the art at the time of

the invention to use the method as taught by Gehring et al. in the network of Aiello et al.

and Bilgic. The motivation for using the method as taught by Gehring et al. in the

network of Aiello et al. and Bilgic being that balance the communication between

devices compatibility in the system.

Conclusion

7.   The prior art made of record and not relied upon is considered pertinent to

applicant's disclosure. Arai et al. (Pat No.: 7068677), Wright (Pat No.: 6771634), and

Bahl (Pat No.: 7009994), are show systems which considered pertinent to the claimed

invention.

Any inquiry concerning this communication or earlier communications from the

examiner should be directed to Kan Yuen whose telephone number is 571-270-2413.

The examiner can normally be reached on Monday-Friday 10:00a.m-3:00p.m EST.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's

supervisor, Ricky O. Ngo can be reached on 571-272-3139. The fax phone number for

the organization where this application or proceeding is assigned is 571-273-8300.

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